

# ACKNOWLEDGEMENT RECEIPT

Electronic Version

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## Title of Invention

## METHOD OF SELECTIVELY BUILDING REDUNDANT LOGIC STRUCTURES TO IMPROVE FAULT TOLERANCE

Submission Type : Utility Patent Filing

Application Number:

10/707323



EFS ID:

51696

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| ISVR1             | Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application  |
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| IGEN77            | Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fuction call. |

First Named Applicant: Kenneth Goodnow

Attorney Docket Number: BUR920020125US1

Timestamp: 2003-12-05 10:36:16 EDT

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File Listing:

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| us-request       | BUR920030125US1-usrequ.xml | 4786         | 2003-12-05               |
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| us-assignment    | BUR920030125US1-usassn.xml | 2476         | 2003-12-05               |
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| us-assignment    | BUR920030125Assign2.tif    | 47052        | 2003-12-05               |
| us-fee-sheet     | BUR920030125US1-usfees.xml | 2078         | 2003-12-05               |
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| us-declaration   | BUR920030125Dec4.tif       | 32524        | 2003-12-05               |
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| application-body | wipo.ent                   | 4956         | 2003-12-05               |
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| application-body | mathml2-qname-1.mod        | 13225        | 2003-12-05               |
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